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APPLICATION NO.	FILING D	ATE	FIRST NAME	D INVENTO	R	ATTORNEY DOCKET NO.	
10/634,008	08/04/2		Chris D.	Atkinson		TI-31132.1	5044
TITLE OF INVENTION: ERROR REDUCTION IN SEMICONDUCTOR PROCESSES							
711 7 = 111 = 1	MALL ENTITY	\$1400	\$30			1700 12/27/2	
Nonprovisional	NO	\$1400	430			,,,,,,,	
EXAMINE	R	ART	UNIT	CLASS-SU			
Chung, Phung M		21	133	714-72	1000		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Atkinson et al.

Docket Number: TI-31132.1

Serial No.: 10/634,008

Art Unit: 2133

Filed: 08/04/2003

Examiner: P. Chung

For: ERROR REDUCTION IN SEMICONDUCTOR PROCESSES

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NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Atkinson et al.		Serial No.: 10/634,008
TITLE OF INVENTION:		7
Error Reduction in Semiconductor Processes		4
TI FILE NO.:	DEPOSIT ACCT, NO.:	1
T1-31132.1	20-0668	
FAXED: 12/17/2004		7
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